

Article

# Single-Switch Bipolar Output DC-DC Converter for Photovoltaic Application

Salvador P. Litrán <sup>1,\*</sup> , Eladio Durán <sup>2</sup> , Jorge Semião <sup>3</sup> and Rafael S. Barroso <sup>1</sup>

<sup>1</sup> Department of Electrical and Thermal Engineering, Design and Projects, University of Huelva, 21001 Huelva, Spain; rafael.barroso@die.uhu.es

<sup>2</sup> Department of Electronic Engineering, Computer Systems and Automation, University of Huelva, 21001 Huelva, Spain; aranda@uhu.es

<sup>3</sup> ISE, Universidade do Algarve, 8005-139 Faro, Portugal/INESC-ID, 1000 Lisboa, Portugal; jsemi@ualg.pt

\* Correspondence: salvador@uhu.es; Tel.: +34-959217585

Received: 23 June 2020; Accepted: 16 July 2020; Published: 18 July 2020



**Abstract:** Bipolar DC grids have become an adequate solution for high-power microgrids. This is mainly due to the fact that this configuration has a greater power transmission capacity. In bipolar DC grids, any distributed generation system can be connected through DC-DC converters, which must have a monopolar input and a bipolar output. In this paper, a DC-DC converter based on the combination of single-ended primary-inductor converter (SEPIC) and Ćuk converters is proposed, to connect a photovoltaic (PV) system to a bipolar DC grid. This topology has, as main advantages, a reduced number of components and a high efficiency. Furthermore, it can contribute to regulate/balance voltage in bipolar DC grids. To control the proposed converter, any of the techniques described in the literature and applied to converters of a single input and single output can be used. An experimental prototype of a DC-DC converter with bipolar output based on the combination of SEPIC and Ćuk converters was developed. On the other hand, a perturb and observe method (P and O) has been applied to control the converter and has allowed maximum power point tracking (MPPT). The combined converter was connected in island mode and in parallel with a bipolar DC microgrid. The obtained results have allowed to verify the behavior of the combined converter with the applied strategy.

**Keywords:** bipolar DC microgrid; DC-DC converter; photovoltaic energy

## 1. Introduction

Nowadays, the use of renewable energies is a reality. According to recent articles of economic/business analysis and electric/electronic industry publications, in many countries, this type of energy represents a high percentage of the energy consumed. Nevertheless, international agreements against climate change suggest that the percentage of energy from renewable resources should increase in the coming years. Furthermore, it is necessary to increase the number of this type of facilities, and improve their efficiency [1,2]. A clear example is the imminent electrification and expansion of the transport sector that makes necessary direct links between renewable energy and electric vehicles (EVs), in grid power.

Within the renewable energies, photovoltaic (PV) energy is one of the most installed. To increase the installed power of this technology, it has been proposed to use more efficient technologies, and reduce the facilities' cost [3,4]. Technological advances in solar power include the increased efficiency [5], the reduction of overheads, reduced size of films, use of polymers, utilization of new materials for solar power cells, improvements of DC-AC converters for connection to the grid [6–9] and new architectures for power management.

There are several possible configurations for PV systems: (i) centralized configuration, where the PV panel can be connected together, and DC power is delivered to one converter; (ii) string configuration, when several PV panels are connected in rows, and each row has its own converter; and (iii) modular configuration, where each PV module is connected to one converter.

The most common PV plant topology consists of a specific module of PV panels connected in series, forming a string or array. The connection of several strings in parallel will configure the corresponding solar plant. There are several configurations for PV systems if we consider the DC bus connection [10]. A monopolar DC configuration uses a single wire; the return conductor may be land or metallic. Its main advantage is the use of a single cable. The PV plant is connected to a monopolar DC network through a DC-DC converter (Figure 1). The converter will adapt the voltage level between the DC bus and the PV modules, so that maximum power is transferred to the system with the adequate control strategy. The energy injected to the DC bus can be transferred to the utility through a DC-AC converter. A different topology for the DC bus is the bipolar DC configuration [11]. It consists of two wires, one with positive polarity and other with negative polarity. In normal operation, the current through ground is zero. It has two voltage levels, allowing, under fault conditions, a monopolar operation. The main advantage of the installation of a bipolar DC bus is the reduction in losses, which makes it possible to reduce the section of the wires, and therefore reduce the conductor volume or increase the transmission capacity. Finally, the DC bus can use a homopolar DC configuration. It has two or more wires with the same polarity, usually negative. The return may be by land or metallic. Its main advantage is the reduction in the costs of insulation; however, the return current is double.

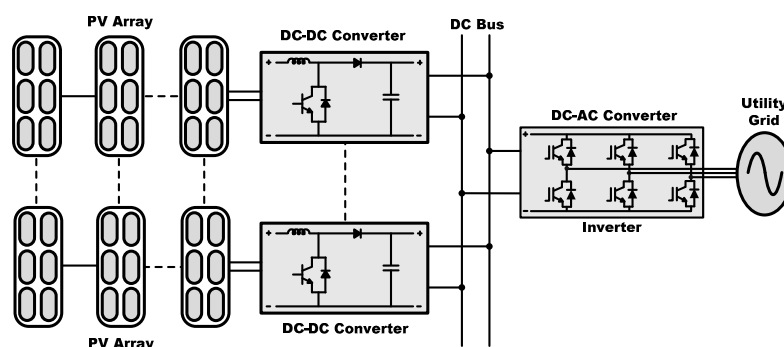


Figure 1. Centralized configuration with monopolar DC bus.

This work proposes the use of a bipolar DC bus instead of the monopolar DC bus, for which a new DC-DC converter topology is proposed, which allows the connection of each photovoltaic string to this bus [12,13].

The connection of a distributed generation system with DC output to a bipolar network can be done in different ways. One of them is to include a DC-DC converter of one input and one bipolar output. When the appropriate control strategy is applied, this option allows setting the generator working point. Regarding the converters, different configurations have been proposed, both in isolated and non-isolated versions. Non-isolated versions are usually more advantageous than isolated versions, from the point of view of the efficiency, cost, volume and weight. The proposed DC-DC converter with one input and two outputs is based on the combination of two converters of one input and one output, such as the single-ended primary-inductor converter (SEPIC) and Ćuk [14–17]. It is a step-down/step-up converter. This topology is effective for this type of application, with high efficiency and with the use of only one controlled power switch. In addition, for the control of the maximum power point, any proposed technique for one input and one output converter can be used [18–24].

The combined SEPIC-Ćuk converter has been theoretically analyzed in [14,15]. However, in this paper, a design procedure is established for the use of these converters in the connection of photovoltaic systems to bipolar DC networks. The operation of the converter is analyzed for a system isolated

and connected to the network. On the other hand, a control strategy has been applied so that the photovoltaic system works at its maximum power point.

In this paper, a comparison between bipolar DC and monopolar DC grids is included in Section 2. In Section 3, the combined converter SEPIC-Ćuk topology is described. In Section 4, an experimental prototype is designed, and a control strategy applied so that the set operates at the point of maximum power transfer. Experimental results are also presented. Finally, Section 5 presents the conclusions of this work.

## 2. Monopolar Versus Bipolar DC Grid

A monopolar DC grid consists of two wires between which a DC voltage is defined. Any load can be connected to this network at the voltage  $V_{dc}$  (Figure 2a).

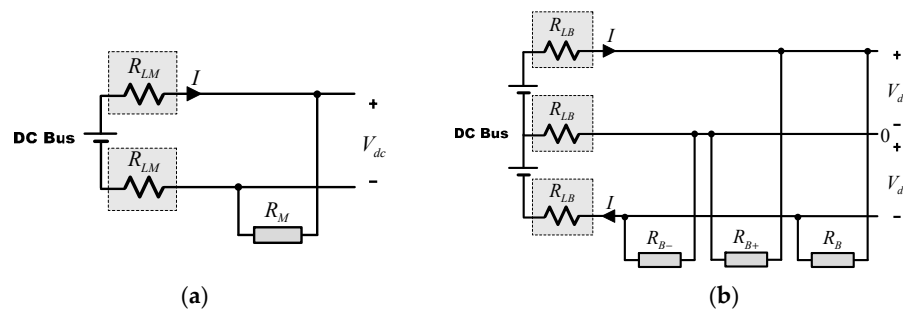


Figure 2. (a) Monopolar DC grid; (b) bipolar DC grid.

On the other hand, the bipolar DC grid (Figure 2b) is formed by three wires, “+”, “-” and “0”. Each load can be connected to two different voltage levels:  $V_{dc}$  if the load is connected between “+” and “0” or between “0” and “-”, and  $2V_{dc}$  if the load is connected between “+” and “-”. Another characteristic to highlight is when the loads connected between “+” and “0” and between “-” and “0” are balanced, the current through the wire “0” is null; therefore, if this criterion is taken in consideration when the loads are connected, the section of this “0” wire can be significantly reduced.

In Figure 2a,  $R_{LM}$  represents the resistance of the wires for a monopolar DC grid. In Figure 2b,  $R_{LB}$  represents the wire resistances for the bipolar DC grid.

When the two topologies are compared, for the same power transmission conditions in both grids and wires of the same section, the power losses in the bipolar DC grid are a quarter of the losses that occur in the monopolar DC grid.

The comparison of the two topologies with the same losses and wires of the same section results that the bipolar DC grid can transmit twice as much power as in the monopolar DC grid.

Finally, when the power losses and the transmission power in both are equal, the relationship between line resistances in both topologies can be calculated. Therefore, the resistance of the bipolar DC grid is four times greater than the resistance of the monopolar DC grid. This means that the wire section in the bipolar grid is a quarter of the monopolar grid.

Therefore, from the point of view of the power losses, the transmission power and the section of the wires, bipolar DC grids have advantages with respect to monopolar DC grids, which are usually used.

## 3. Proposed Converter

The connection of a distributed generation system with DC output to a bipolar network can be done in different ways. One of them is to include a DC-DC converter of one input and one bipolar output. When the appropriate control strategy is applied, this option allows setting the generator working point. For the connection of a PV system to a bipolar grid, different solutions based on DC-DC converters have been analyzed [8,9]. Regarding the converters, different configurations have been proposed, both in isolated and non-isolated versions. Non-isolated versions are usually more

advantageous than isolated versions, from the point of view of the efficiency, cost, volume and weight. In this sense, various configurations with bipolar output have been proposed in the literature, all of them have in common the use several power switches and several reactive components. In [11,25], a three-level boost converter was reported, to provide two bipolar output voltages. It required an inductor, two switches (one of them floating) and two diodes. In [26], a three-level boost converter with two inductors was shown. It provided two bipolar output voltages also. Three-level boost converters are widely used in low-voltage bipolar-type DC microgrids. In the same way, a three-level DC-DC converter with coupled inductors for high-power applications was proposed in [27]. A single-inductor power converter with buck–boost capability, two switches (one of them floating) and three diodes was presented in [28], where the switches operated in overlapping mode. A bipolar output DC-DC converter was proposed in [29], consisting of two basic DC-DC converters. The first one was a boost converter and the second was a DC-DC buck–boost converter. Both operated in a complementary way, in a series-combined boost and buck–boost converter. A dual-output DC-DC boost converter was reported in [30], where it employed two inductors, two diodes and two switches.

In this work, a converter based on the combination of SEPIC and Ćuk converters is proposed. Figure 3 shows the configuration of this converter. This combination is based on the fact that both converters have the same input, one power switch and one input inductor. They have the same conversion ratio and the output of the Ćuk converter is inverted with respect to that of the SEPIC. Therefore, its combination allows obtaining a bipolar output voltage from a monopolar input.

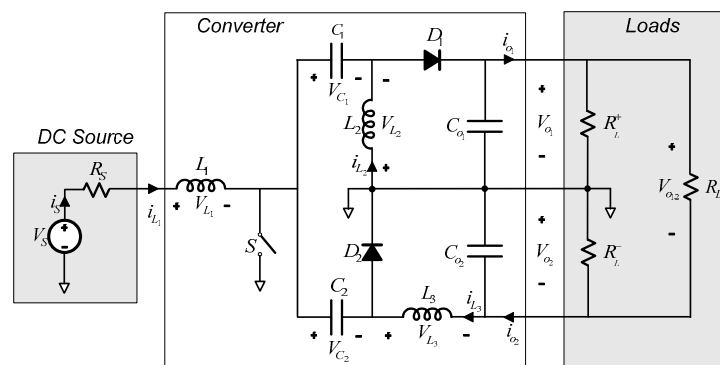


Figure 3. SEPIC (single-ended primary-inductor converter)-Ćuk combination converter.

This configuration employs only one power switch, three inductors, two link capacitors and two diodes. It has an advantage over the other proposals in that it has a reduced number of components. Only one power switch is necessary, which has its gate referenced to ground what simplifies the design of the gate drivers. This makes both the power switch and the input inductor concentrate all the input current, and it is also possible to couple several power switches and several inductors in parallel to distribute the current, allowing a synchronous or overlapping mode (the same control signal) or interleaved mode (shifting phase the control signals of each switch).

The converter can work in two operating modes: continuous conduction mode (CCM) and discontinuous conduction mode (DCM). CCM means that the inductor current is continuous. On the other hand, in DCM, the current of the inductor is discontinuous, i.e., in a time interval of the switching period, this current is zero.

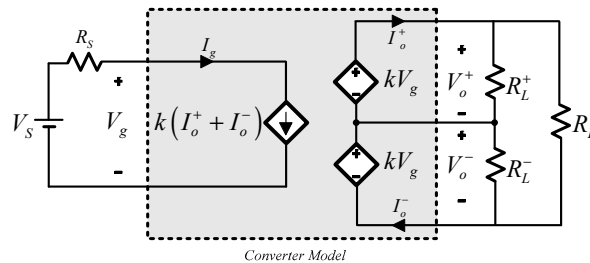
The operation of the converter, in steady state, can be analyzed through a simple model with controlled sources. Figure 4 shows the equivalent circuit.  $R_s$  and  $V_s$  represent the equivalent of the DC source. The relationship between input and output variables can be expressed as

$$\begin{bmatrix} I_g \\ V_o^+ \\ V_o^- \end{bmatrix} = \begin{bmatrix} k & k & 0 \\ 0 & 0 & k \\ 0 & 0 & k \end{bmatrix} \begin{bmatrix} I_o^+ \\ I_o^- \\ V_g \end{bmatrix} \tag{1}$$

where the parameter  $k$  depends on the operating mode of the converter. If the converter works in CCM,  $k$  must be

$$k = \frac{D}{1 - D} \tag{2}$$

where  $D$  is the duty cycle.



**Figure 4.** Converter model with controlled sources.

In the case of DCM, the value of  $k$  is given by the expression

$$k = D \sqrt{\frac{R_o T_S}{2L_{eq}}} \tag{3}$$

Here,  $T_S$  is the switching period and according to Figure 3,  $R_o$  and  $L_{eq}$  are given by

$$\frac{1}{R_o} = \frac{1}{R_L^+} + \frac{1}{R_L^-} + \frac{4}{R_L} \tag{4}$$

$$\frac{1}{L_{eq}} = \frac{1}{L_1} + \frac{1}{L_2} + \frac{1}{L_3} \tag{5}$$

In PV applications, the converter works by following the maximum power point of the source. For this, the following condition must be fulfilled:

$$R_S = R_g \tag{6}$$

Here,  $R_g$  is the equivalent resistance from the terminal of the controlled current source.

A PV system in isolated operation can be represented by the circuit shown in Figure 4. At the input loop, the current is given by

$$I_g = k(I_o^+ + I_o^-) = k \left[ \frac{V_o^+}{R_L^+} + \frac{V_o^-}{R_L^-} + \frac{2(V_o^+ + V_o^-)}{R_L} \right] \tag{7}$$

That can be expressed in the way of

$$I_g = k^2 V_g \left( \frac{1}{R_L^+} + \frac{1}{R_L^-} + \frac{4}{R_L} \right) \tag{8}$$

Therefore, according Equation (6), it must fulfill

$$R_S = \frac{R_o}{k^2} \tag{9}$$

Equation (9) is useful when the system is operating in isolated mode, since it allows to determine the duty cycle under different load conditions when the system is transferring the maximum power.

In many applications, the PV system is connected to a bipolar grid in parallel with other sources. When the same model of controlled sources is considered, the equivalent circuit from the bipolar network is useful because it allows the analysis of the power flows and establishing the range of powers when the converter works in CCM mode. In this case, from the terminals of the bipolar grid, the equivalent circuit of the converter can be obtained. Figure 5 shows the equivalent voltage, which is  $kV_S$ , and the value equivalent resistance,  $2k^2R_S$ .

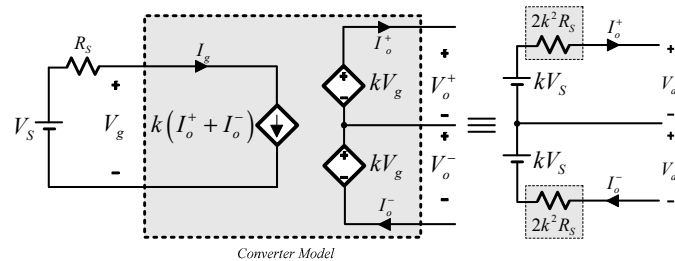


Figure 5. Converter model from output terminals.

Under the conditions of maximum power

$$\begin{aligned} V_S &= 2V_g \\ P &= \frac{V_g^2}{R_S} \end{aligned} \tag{10}$$

where  $V_g$  is the output voltage of the PV system when the maximum power  $P$  is transferred.

For a balanced bipolar grid of voltage  $\pm V_{dc}$ , at the positive output, transferred power can be expressed by

$$P^+ = \frac{P}{2} = \frac{V_g^2}{2R_S} = V_{dc}I_o^+ \tag{11}$$

So, from the equivalent circuit in Figure 5, the next equation can be established as

$$\frac{2kV_g - V_{dc}}{2k^2R_S} = I_o^+ \tag{12}$$

When  $I_o^+$  is substituted in Equation (12) from Equation (11)

$$\frac{2kV_g - V_{dc}}{2k^2R_S} = \frac{V_g^2}{2R_S V_{dc}} \tag{13}$$

Obtaining  $k$  from Equation (13) and taking into account that the system must supply power, it is deduced

$$k = \frac{V_{dc}}{V_g} \tag{14}$$

Therefore,  $k$  only depends on the voltage at the connection point of the converter and the output voltage of the photovoltaic system at the maximum power point (MPP).

Equation (14) allows to obtain the duty cycle of the converter for different conditions of grid voltage and input voltage at the MPP.

From the point of view of the converter component design, Table 1 shows the average values of voltages and currents for each of the converter elements when it works in steady state. It is assumed that the converter operates in CCM. This mode of operation is the preferred in power systems due to its higher efficiency, so this operation mode is the most adequate for the proposed application. It should be noted that the voltage at the  $C_2$  capacitor (Ćuk side) is higher than the voltage at the  $C_1$  capacitor (SEPIC side). Choosing the appropriate values for the inductors and capacitors is of special importance for the proper operation of the converter.

**Table 1.** Performance of the combination of SEPIC (single-ended primary-inductor converter) and Ćuk converters.

|                                | Switch                              | D <sub>1</sub>                    | D <sub>2</sub>                    |
|--------------------------------|-------------------------------------|-----------------------------------|-----------------------------------|
| Semiconductor Voltages         | $V_g + V_o^+$                       | $V_g + V_o^+$                     | $V_g + V_o^-$                     |
| Semiconductor Average Currents | $\frac{V_g D^2}{R_o \cdot (1-D)^2}$ | $\frac{V_g D}{R_o^+ \cdot (1-D)}$ | $\frac{V_g D}{R_o^- \cdot (1-D)}$ |
| -                              | C <sub>1</sub>                      | C <sub>2</sub>                    | -                                 |
| Capacitor Average Voltages     | $V_g$                               | $V_g + V_o^-$                     | -                                 |
| -                              | L <sub>1</sub>                      | L <sub>2</sub>                    | L <sub>3</sub>                    |
| Inductor Average Currents      | $\frac{V_g D^2}{R_o \cdot (1-D)^2}$ | $\frac{V_g D}{R_o^+ \cdot (1-D)}$ | $\frac{V_g D}{R_o^- \cdot (1-D)}$ |

Where:  $\frac{1}{R_o} = \frac{1}{R_L^+} + \frac{1}{R_L^-} + \frac{4}{R_L}$ ;  $\frac{1}{R_o^+} = \frac{1}{R_L^+} + \frac{2}{R_L}$ ;  $\frac{1}{R_o^-} = \frac{1}{R_L^-} + \frac{2}{R_L}$

The selection of the L<sub>1</sub> inductor depends on the desired ripple value of the input current. Its peak-to-peak value is given by

$$\Delta i_{L1} = \frac{V_g}{L_1} DT_S \tag{15}$$

Δi<sub>L1</sub> is a percentage of the input current average.

For the choice of the L<sub>2</sub> and L<sub>3</sub> inductors, it is assumed that the converter works in CCM in a power range.

$$I_{L1} = \frac{V_g D^2}{R_o \cdot (1 - D)^2} \tag{16}$$

On the other hand, the combination of SEPIC and Ćuk converters works in CCM when the next expressions are fulfilled as

$$\frac{2L_{eq}}{R_o T_S} > (1 - D)^2 \tag{17}$$

where L<sub>eq</sub> and R<sub>o</sub> are defined according Equation (5).

The selection of the link capacitors C<sub>1</sub> and C<sub>2</sub>, for the SEPIC and Ćuk sides, is based on assuming that the voltage in those capacitors must be constant. The resonant frequencies (ω<sub>r</sub>) must be lower than the switching frequency (ω<sub>S</sub>), in order to ensure constant voltage.

To ensure that the converter operation mode is neither discontinuous quasi-resonant mode (DQRM) nor discontinuous capacitor voltage mode (DCVM), the resonant frequency must be

$$\begin{aligned} \omega_S > \omega_r; \text{ with } \omega_{r1}^2 &= \frac{1}{C_1(L_1+L_2)} \text{ for SEPIC side,} \\ \omega_{r2}^2 &= \frac{1}{C_2(L_1+L_3)} \text{ for Ćuk side} \\ \text{furthermore : } C_1 &> \frac{D^2 T_S}{2R_o} \text{ and } C_2 > \frac{D^2 T_S}{2R_o} \end{aligned} \tag{18}$$

In the same way, the voltage ripple in C<sub>1</sub> and C<sub>2</sub> is given by

$$\begin{aligned} \Delta v_{C1} &= \frac{V_g \cdot D^2}{(1-D) \cdot R_o \cdot C_1 \cdot f_S} \Rightarrow \frac{\Delta v_{C1}}{V_{o1}} = \frac{D}{R_o \cdot C_1 \cdot f_S}, \text{ SEPIC} \\ \Delta v_{C2} &= \frac{V_g \cdot D^2}{(1-D) \cdot R_o \cdot C_2 \cdot f_S} \Rightarrow \frac{\Delta v_{C2}}{V_{o2}} = \frac{D}{R_o \cdot C_2 \cdot f_S}, \text{ Ćuk} \end{aligned} \tag{19}$$

The selection of the output capacitors  $C_{o1}$  and  $C_{o2}$  is also made to obtain the desired voltage ripple value. Its peak-to-peak value is given by

$$\begin{aligned}\frac{\Delta v_{o1}}{V_{o1}} &= \frac{D}{R_o^+ \cdot C \cdot f_s}, \text{ for SEPIC side} \\ \frac{\Delta v_{o2}}{V_{o2}} &= \frac{(1-D)}{8 \cdot L_2 \cdot C \cdot f_s^2}, \text{ for Ćuk side}\end{aligned}\quad (20)$$

#### 4. Experimental Prototype

An experimental prototype of the proposed DC-DC converter has been designed, based on the circuit shown in Figure 3. The prototype is a scaled model that allows the verification of its operation in the laboratory. Figure 6 shows the experimental prototype.

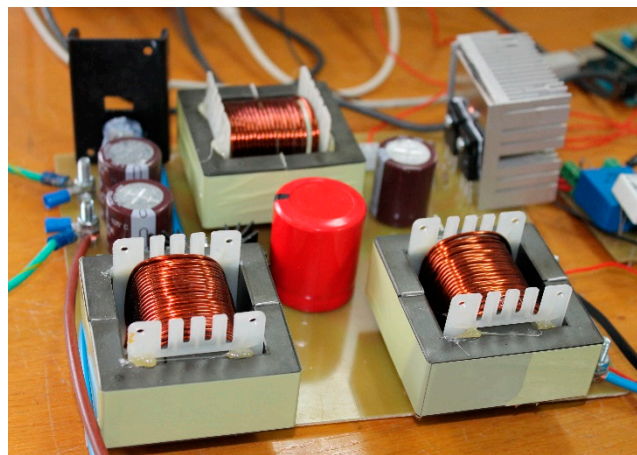


Figure 6. Experimental prototype.

The designed converter allows the connection of a PV system to a bipolar DC microgrid of  $\pm 100$  V. In this design, an array consisting of three monocrystalline photovoltaic modules from Isfoton, model ISF-250, have been considered. Table 2 shows its performance at an irradiance of  $1000 \text{ W/m}^2$  and cell temperature of  $25 \text{ }^\circ\text{C}$ .

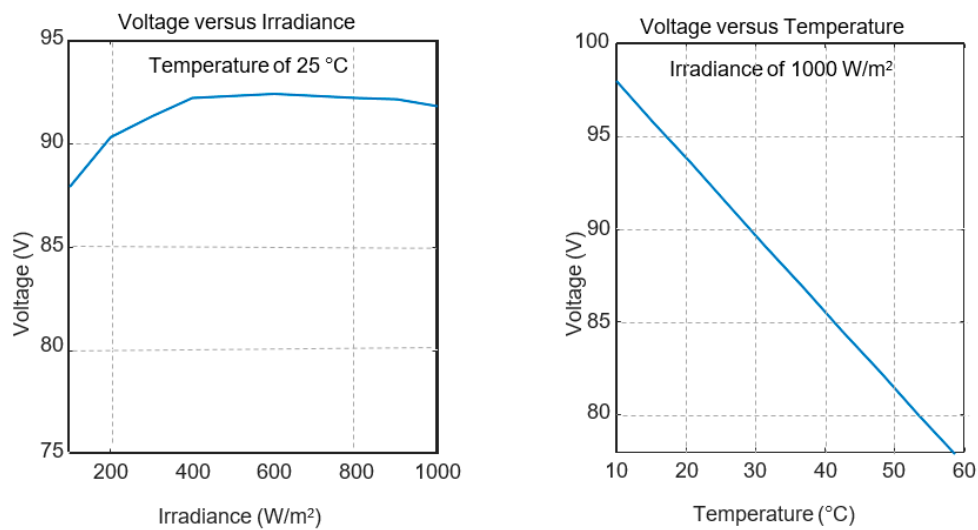
Table 2. ISF-250 model. Performance at an irradiance of  $1000 \text{ W/m}^2$  and cell temperature of  $25 \text{ }^\circ\text{C}$ .

|   |        |
|---|--------|
| Rated Power ( $P_{max}$ )                 | 250 W  |
| Open Circuit Voltage ( $V_{oc}$ )         | 37.8 V |
| Short-Circuit Current ( $I_{sc}$ )        | 8.75 A |
| Maximum Power Point Voltage ( $V_{max}$ ) | 30.6 V |
| Maximum Power Point Current ( $I_{max}$ ) | 8.17 A |
| Efficiency                                | 15.1%  |

For this string, the open circuit voltage is 114 V and short-circuit current is 8.75 A.

In the maximum power conditions, the voltage varies from 85 to 92.5 V, depending on the irradiance, for a temperature of  $25 \text{ }^\circ\text{C}$ . On the other hand, when the irradiance is  $1000 \text{ W/m}^2$ , the voltage varies from 75 to 98 V, according to the temperature. Figure 7 shows the variation in the output voltage for different irradiances and different temperatures.

With respect to the power, it varies from 72 ( $100 \text{ W/m}^2$ ,  $25 \text{ }^\circ\text{C}$ ) to 796 W ( $1000 \text{ W/m}^2$ ,  $10 \text{ }^\circ\text{C}$ ), which will be taken as design values to the converter.



**Figure 7.** Characteristic curves for maximum power of photovoltaic string of 3 series modules of Isofoton ISF-250.

4.1. Converter Design

The selection of the components of the SEPIC-Ćuk combined converter shown in Figure 3 is established according the peak power of the string where the converter will be connected. Therefore, the prototype will be designed for a power of 1 kW (0.25 kW above maximal power). The voltage at maximum power for different irradiance can vary from 75 to 98 V. Thus, according to Equation (14), the duty cycle of the converter will be between 0.51 and 0.57. The switching period is established at 40  $\mu$ s. Table 3 shows the main design features of the converter.

**Table 3.** Main features of the converter.

|                         |         |
|-------------------------|---------|
| Rated Power             | 1000 W  |
| Maximum Input Current   | 8.2 A   |
| Range of Input Voltages | 88–98 V |
| Rated Output Voltage    | 100 V   |
| Switching Frequency     | 25 kHz  |

The maximum input current is given for the maximum power, it is 8.17 A. On the other hand, the maximum ripple of the input current is set to 4%. The application of Equation (15) results in an inductance,  $L_1$ , of 5 mH.

For the lower power, 72 W, the input voltage is 88 V (100 W/m<sup>2</sup>, 25°C), and the maximum load  $R_o$  can be calculated, which results in  $R_o = 103 \Omega$ .

If  $R_o^+ = R_o^- = R_o = 103 \Omega$ , according to Equation (17), the inductance  $L_{eq}$  must be greater than 0.47 mH. Furthermore, considering  $L_2 = L_3$ , its values will be of 1.03 mH. In this design,  $L_2 = L_3 = 1$  mH was chosen.

For a switching period ( $T_s$ ) of 40  $\mu$ s and  $f_s = 100f_r$ , the Equation (18) are satisfied for a minimum capacity value of 101  $\mu$ F (for  $C_1$  and  $C_2$ ). Therefore, a standard value of 470  $\mu$ F is chosen. With respect to the voltage ripple, the application of Equation (19) with  $C_1 = C_2 = 470 \mu$ F allows to obtain a ripple of 0.4%, when the load is maximum, which is when  $D = 0.52$  and  $R_o = 11.1 \Omega$ .

Finally, for the selection of the output capacitors  $C_{o1}$  and  $C_{o2}$ , Equation (20) is applied. For a low-voltage ripple,  $C = 470 \mu$ F has been considered, which provides an estimated voltage ripple of 0.4% for  $V_o^+$  and 0.1% for  $V_o^-$ .

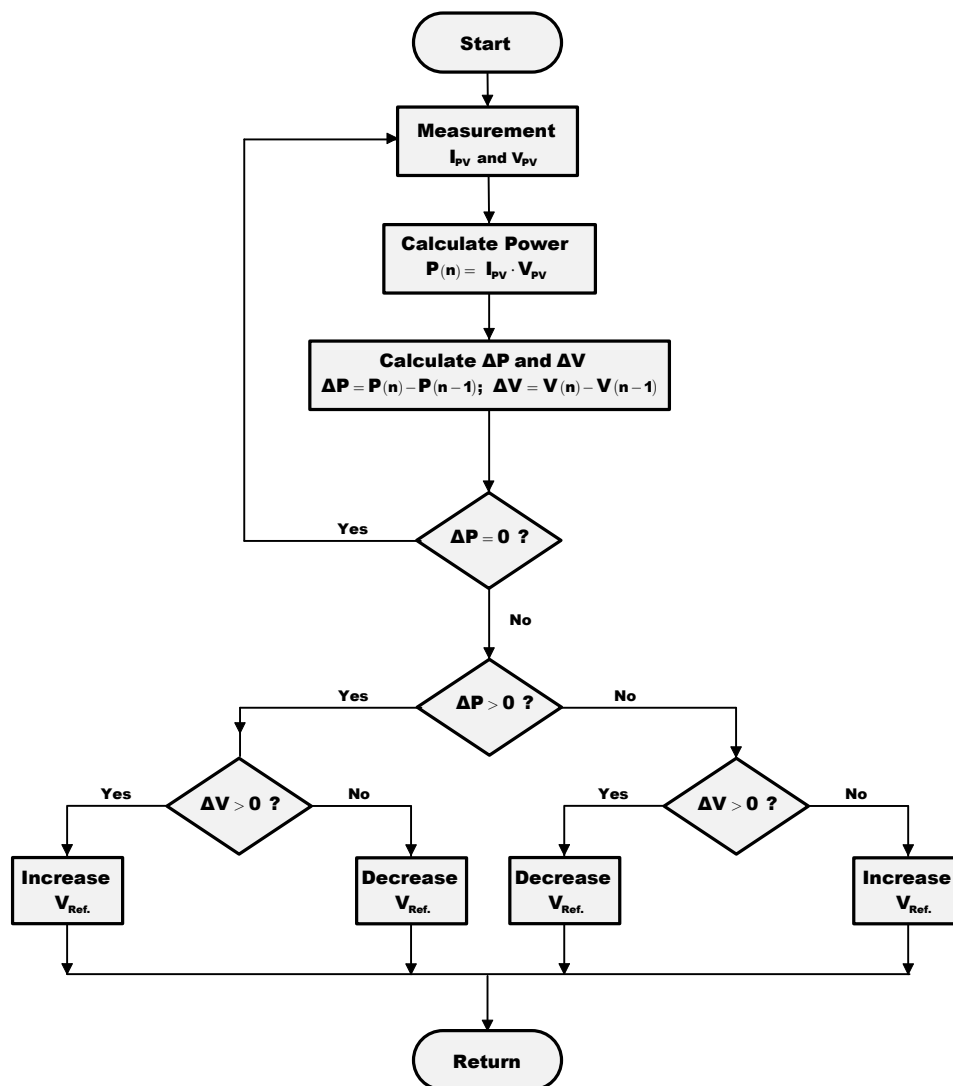
Table 4 summarizes the characteristics of the SEPIC-Ćuk combined converter components implemented.

**Table 4.** DC-DC combined converter components.

|            |  |
|------------|--|
| $D_1, D_2$ | MUR1560, 600 V (ON Semiconductor, Denver, Colorado, USA), 15 A     |
| $S$        | IGBT50N60T (Infineon Technologies AG, Munich, Germany) 600 V, 50 A |
| $L_1$      | 5.0 mH, 20 A   |
| $L_2, L_3$ | 1.0 mH, 10 A   |
| $C_1$      | 470 $\mu$ F, 250 V   |
| $C_2$      | 470 $\mu$ F, 400 V   |
| $C$        | 470 $\mu$ F, 250 V   |

4.2. Control Strategy

For the control of the converter, several strategies can be applied to the proposals in the technical literature for converters of one input and one output [18–24]. In this work, the technique chosen allows maximum power point tracking. The perturb and observe method (P and O) is one of the most used in practice [24]. It is an iterative method where the reference voltage varies according to the increase in power, obtained from the input voltage and current to the converter. Figure 8 shows the flowchart of the P and O strategy.



**Figure 8.** Perturb and observe method flowchart.

The control strategy has been implemented in the TMS320F28335 Delfino Microcontroller by Texas Instruments Dallas, Texas, USA. The measuring sensors used are the LA 25-NP for the currents and the LV 25-1000 for the voltage, both by LEM International SA, Geneva, Switzerland.

### 4.3. Experimental Results

The experimental system set-up is shown in Figure 9. The DC-DC converter will connect to a bipolar DC microgrid of  $\pm 100$  V. It has been designed for a maximum power of 5 kW. In the test, three resistors have been connected. They have the following values:  $R_L^+ = R_L^- = 47/2 \Omega$  and  $R_L = 47 \Omega$ .

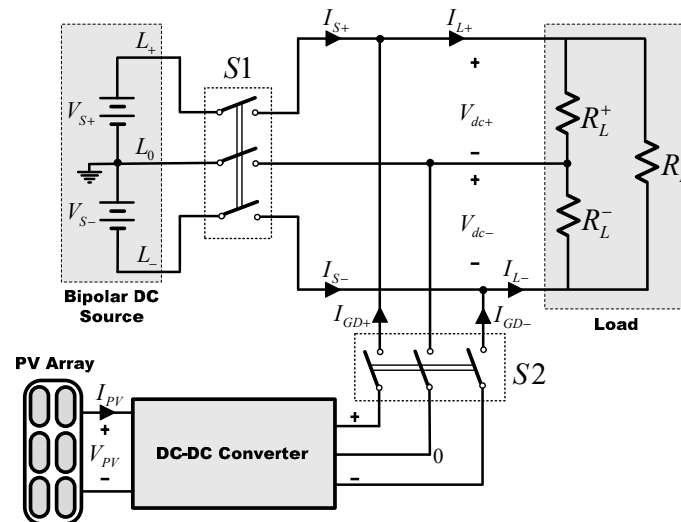


Figure 9. Experimental system.

When the switch S1 is closed and S2 is open, the  $V_{dc+}$  and  $V_{dc-}$  voltages have an average value of 100 V. The current for each conductor is  $I_L^+ = 8.5$  A and  $I_L^- = -8.5$  A, so that the power consumed by the set is 1.7 kW.

The PV system is emulated by a DC source, so the point of maximum power can be adjusted to a desired value, modifying its internal resistance and voltage.

Figure 10 shows the curves  $I-V$  and  $P-V$  of a photovoltaic string of three series of modules of Isofoton ISF-250 for a given irradiance. In this situation, the maximum power is given for 750 W with a voltage of 91.8 V and a current of 8.2 A.

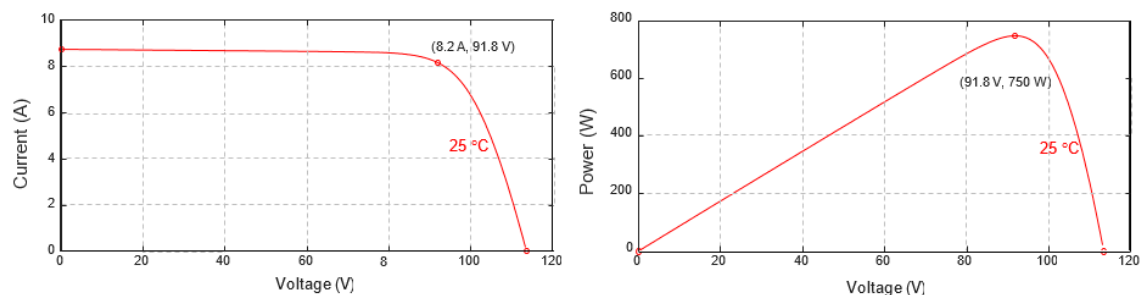
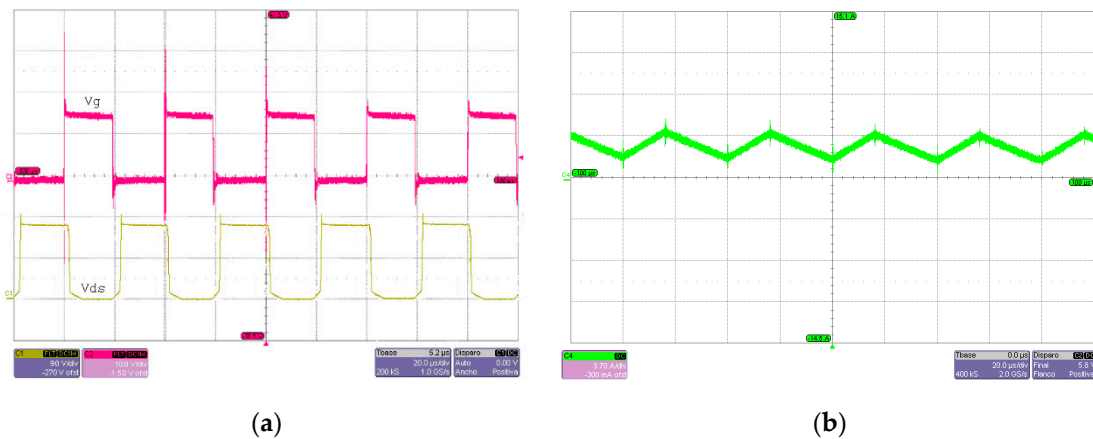


Figure 10. Curves  $I-V$  and  $P-V$  of one photovoltaic string of 3 series of modules of Isofoton ISF-250. Irradiance  $1000 \text{ W/m}^2$

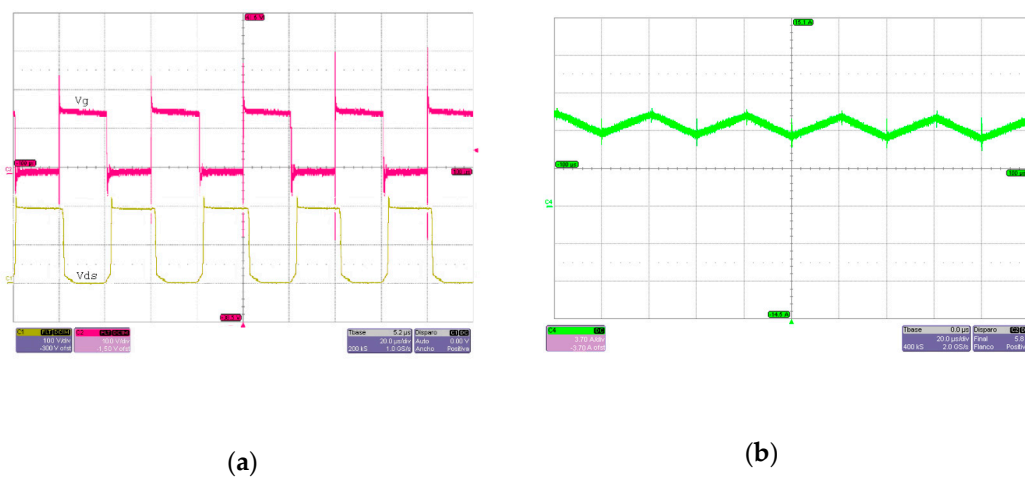
The DC source is adjusted to an output voltage of 182.4 V, with an internal resistance ( $R_s$ ) of  $11.1 \Omega$ . Therefore, the DC source will transfer the maximum power (750 W) when it supplies a current of 8.17 A and with an output voltage of 91.8 V.

In the following test, the switch S1 is open and the switch S2 is closed. Thus, the PV system is operating in island mode. According with Equation (4),  $R_o = 5.9 \Omega$  for the resistance values  $R_L^+$ ,  $R_L^-$  and  $R_L$ . Therefore, the application of Equation (9) let the obtaining of  $k = 0.73$ , resulting in a theoretical duty cycle of 42%. The measured power is 638 W, the voltage  $V_{dc+} = -V_{dc-} = 62.5 \text{ V}$  and the currents  $I_{L+} = -I_{L-} = 5.1 \text{ A}$ . With respect to the current ( $I_{PV}$ ) and voltage ( $V_{PV}$ ) at the input converter, their average values are 8.2 A and 92.1 V, respectively. Thus, the efficiency is 84.4%. Figure 11 shows the gate source signal,  $V_g$ , drain source voltage,  $V_{ds}$ , and the input current waveform. The measured duty cycle in these conditions is 41%, similar to the theoretical value.



**Figure 11.** Gate source signal,  $V_g$ , and drain source voltage,  $V_{ds}$  (a), and input current waveform (b) when the photovoltaic system operates in island mode.

When the PV system and source are connected (Figure 9, S1 and S2 are closed), the voltages at the point of connection are  $V_{dc+} = -V_{dc-} = 99.7 \text{ V}$ . From the theoretical point of view, Equation (14) must be fulfilled, therefore at the maximum power point,  $k$  must be 1.09 and the theoretical duty cycle will be 52%. The converter supplies a current  $I_{GD+} = -I_{GD-} = 3.2 \text{ A}$ , so the power transferred by the converter is 638 W. The input voltage ( $V_{PV}$ ) is 92 V and the input current is ( $I_{PV}$ ) 8.1 A. Figure 12 shows the gate source signal,  $V_g$ , drain source voltage,  $V_{ds}$ , and the input current waveform. In this case, the measured duty cycle is about 51%. In this test, the measured efficiency of the converter is 86.5%.



**Figure 12.** Gate source signal,  $V_g$ , and drain source voltage,  $V_{ds}$  (a), and input current waveform (b) when the photovoltaic system operates in parallel with a bipolar DC source.

## 5. Conclusions

Bipolar DC grids have become an adequate solution for high-power DC microgrids. When compared with monopolar DC grids, their main advantage lies in a greater power transmission capacity.

A distributed generation system can be connected to the bipolar DC microgrid through DC-DC converters. There are different converter configurations with bipolar output, however, all of them have several power switches. In this paper, a converter based on the combination of a SEPIC and a Ćuk converter is proposed. This topology has, as main advantages, a reduced number of components and the use of a single switch.

In the proposed converter control strategy, any of the techniques described in the literature applied to converters of a single output can be used. Among the different control strategies applied to PV systems, the most commonly used are those that allow maximum power point tracking.

In this paper, an experimental prototype DC-DC converter with bipolar output based on the combination of SEPIC and Ćuk converters was developed. This converter allows the connection of a PV system to a bipolar DC grid. In the control, the perturb and observe method has been applied which has allowed maximum power point tracking. The results obtained allowed to verify the behavior of the converter with the applied strategy.

**Author Contributions:** All of the Authors conceived and designed the experiments; performed the experiments; analyzed the data; contributed reagents/materials/analysis tools; wrote the paper. All authors have read and agreed to the published version of the manuscript.

**Funding:** This research received no external funding.

**Conflicts of Interest:** The authors declare no conflict of interest.

## References

1. Chakraborty, C.; Iu, H.H.-C.; Lu, D.D.-C. Power converters, control, and energy management for distributed generation. *IEEE Trans. Ind. Electron.* **2015**, *62*, 4466–4470. [[CrossRef](#)]
2. Justo, J.J.; Mwasilu, F.; Lee, J.; Jung, J.W. AC-microgrids versus DC-microgrids with distributed energy resources: A review. *Renew. Sustain. Energy Rev.* **2013**, *24*, 387–405. [[CrossRef](#)]
3. El-Shahat, A.; Sumaiya, S. DC-Microgrid System Design, Control, and Analysis. *Electronics* **2019**, *8*, 124. [[CrossRef](#)]
4. Kamel, A.A.; Rezk, H.; Shehata, N.; Thomas, J. Energy management of a DC microgrid composed of photovoltaic/fuel cell/battery/supercapacitor systems. *Batter* **2019**, *5*, 63. [[CrossRef](#)]
5. Kurtz, S.; Repins, I.; Metzger, W.K.; Verlinden, P.J.; Huang, S.; Bowden, S.; Tappan, I.; Emery, K.; Kazmerski, L.L.; Levi, D. Historical Analysis of Champion Photovoltaic Module Efficiencies. *IEEE J. Photovolt.* **2018**, *8*, 363–372. [[CrossRef](#)]
6. Carrasco, J.M.; Franquelo, L.G.; Bialasiewicz, J.T.; Galvan, E.; Portillo, R.; Prats, M.A.M.; Leon, J.I.; Moreno-Alfonso, N. Power-electronic systems for the grid integration of renewable energy sources: A survey. *IEEE Trans. Ind. Electron.* **2006**, *53*, 1002–1016. [[CrossRef](#)]
7. Zapata, J.W.; Kouro, S.; Carrasco, G.; Meynard, T.A. Step-down partial power dc-dc converters for two-stage photovoltaic string inverters. *Electronics* **2019**, *8*, 87. [[CrossRef](#)]
8. Norambuena, M.; Kouro, S.; Dieckerhoff, S.; Rodríguez, J. Reduced multilevel converter: A novel multilevel converter with a reduced number of active switches. *IEEE Trans. Ind. Electron.* **2017**, *65*, 3636–3645. [[CrossRef](#)]
9. Lakshmi, M.; Hemamalini, S. Nonisolated High Gain DC–DC converter for DC microgrids. *IEEE Trans. Ind. Electron.* **2017**, *65*, 1205–1212. [[CrossRef](#)]
10. Rodriguez, P.; Rouzbehi, K. Multi-terminal DC grids: Challenges and prospects. *J. Mod. Power Syst. Clean Energy* **2017**, *5*, 515–523. [[CrossRef](#)]
11. Kakigano, H.; Miura, Y.; Ise, T. Low-Voltage Bipolar-Type DC microgrid for super high quality distribution. *IEEE Trans. Power Electron.* **2010**, *25*, 3066–3075. [[CrossRef](#)]
12. Kim, J.-Y.; Kim, H.-S.; Baek, J.-W.; Jeong, D.-K. Analysis of effective three-level neutral point clamped converter system for the bipolar LVDC Distribution. *Electronics* **2019**, *8*, 691. [[CrossRef](#)]

13. Aman, A.; Dwivedi, R.S.; Shah, J.D.; Singh, R.K.; Mahanty, R. Digitally controlled bipolar converter for DC micro grids. In Proceedings of the IECON 2017—43rd Annual Conference of the IEEE Industrial Electronics Society, Beijing, China, 29 December–1 November 2017; pp. 2258–2263.
14. Ferrera, M.B.; Litran, S.P.; Duran, E.; Enrique, J.M. New single-input, multiple-output converter topologies: Combining single-switch nonisolated DC-DC converters for single-input, multiple-output applications. *IEEE Ind. Electron. Mag.* **2016**, *10*, 6–20. [[CrossRef](#)]
15. Ferrera, M.B.; Litrán, S.P.; Durán, E.; Andújar, J.M. A converter for bipolar DC link based on SEPIC-Ćuk combination. *IEEE Trans. Power Electron.* **2015**, *30*, 6483–6487. [[CrossRef](#)]
16. Patra, P.; Patra, A.; Misra, N. A single-inductor multiple-output switcher with simultaneous buck, boost, and inverted outputs. *IEEE Trans. Power Electron.* **2011**, *27*, 1936–1951. [[CrossRef](#)]
17. Anand, A.; Singh, B.P. Power factor correction in Cuk-SEPIC-based dual-output-converter-fed SRM drive. *IEEE Trans. Ind. Electron.* **2017**, *65*, 1117–1127. [[CrossRef](#)]
18. Esrām, T.; Chapman, P.L. Comparison of photovoltaic array maximum power point tracking techniques. *IEEE Trans. Energy Convers.* **2007**, *22*, 439–449. [[CrossRef](#)]
19. Raghavendra, K.V.G.; Zeb, K.; Muthusamy, A.; Krishna, T.N.V.; Kumar, S.V.S.V.P.; Kim, D.-H.; Kim, M.-S.; Cho, H.-G.; Kima, H.-J.; Raghavendra, K.V.G. A comprehensive review of DC-DC converter topologies and modulation strategies with recent advances in solar photovoltaic systems. *Electronics* **2019**, *9*, 31. [[CrossRef](#)]
20. Robles-Algarín, C.; Hernández, D.S.; Leal, D.R. A low-cost maximum power point tracking system based on neural network inverse model controller. *Electronics* **2018**, *7*, 4. [[CrossRef](#)]
21. Furtado, A.M.S.; Bradascchia, F.; Cavalcanti, M.C.; Limongi, L.R. A reduced voltage range global maximum power point tracking algorithm for photovoltaic systems under partial shading conditions. *IEEE Trans. Ind. Electron.* **2018**, *65*, 3252–3262. [[CrossRef](#)]
22. Zadeh, M.J.Z.; Fathi, S.; Jedari, M. A new approach for photovoltaic arrays modeling and maximum power point estimation in real operating conditions. *IEEE Trans. Ind. Electron.* **2017**, *64*, 9334–9343. [[CrossRef](#)]
23. Ramyar, A.; Iman-Eini, H.; Farhangi, S. Global maximum power point tracking method for photovoltaic arrays under partial shading conditions. *IEEE Trans. Ind. Electron.* **2016**, *64*, 2855–2864. [[CrossRef](#)]
24. Kumar, N.; Hussain, I.; Singh, B.P.; Panigrahi, B. Framework of maximum power extraction from solar PV panel using self predictive perturb and observe algorithm. *IEEE Trans. Sustain. Energy* **2017**, *9*, 895–903. [[CrossRef](#)]
25. Zhang, M.T.; Jiang, Y.; Lee, F.C.; Jovanović, M.M. Single-phase three-level boost power factor correction converter. In Proceedings of the 1995 IEEE Applied Power Electronics Conference and Exposition—APEC'95, Dallas, TX, USA, 5–9 March 1995; pp. 434–439.
26. Haddad, K. Three level DC-DC converters as efficient interface in two stage PV power systems. In Proceedings of the 2012 IEEE Energytech, Cleveland, OH, USA, 30–31 May 2012; pp. 1–6.
27. Lu, S.; Mu, M.; Jiao, Y.; Lee, F.C.; Zhao, Z. Coupled inductors in interleaved multiphase three-level DC-DC converter for high-power applications. *IEEE Trans. Power Electron.* **2015**, *31*, 120–134. [[CrossRef](#)]
28. Schoenbauer, S.; Martin-Lopez, F.R. Single Inductor Buck Boost Converter with Positive and Negative Outputs. U.S. Patent 2010,003,9080, 18 February 2010.
29. Duran-Gomez, J.; Garcia-Cervantes, E.; Lopez-Flores, D.; Enjeti, P.; Palma, L. Analysis and Evaluation of a Series-Combined Connected Boost and Buck-Boost DC-DC Converter for Photovoltaic Application. In Proceedings of the Twenty-First Annual IEEE Applied Power Electronics Conference and Exposition, APEC '06, Dallas, TX, USA, 19–23 March 2006; pp. 19–23.
30. Rezayi, S.; Iman-Eini, H.; Hamzeh, M.; Bacha, S.; Farzamkia, S. Dual-output DC/DC boost converter for bipolar DC microgrids. *IET Renew. Power Gener.* **2019**, *13*, 1402–1410. [[CrossRef](#)]

